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U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

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APPLICANTS: Zaboronski Oleg; Meulemans Peter;					
CONTINUING DATA VERIFIED: New New					
FOREIGN APPLICATIONS VERIFIED: UNITED KINGDOM 0130255.3 12/18/2001 New					
PG-PUB DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>		ATTORNEY DOCKET NO 1365.059US1	
TITLE: Logic circuits for performing modular multiplication and exponentiation					

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NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
3/17/05		Assistant Examiner	Total Claims 106
ISSUE FEE			Print Claim for 0.6 / 1
Amount Due \$1000.00	Date Paid	TAN V. MAI PRIMARY EXAMINER New Primary Examiner	DRAWING Sheets Drawg. 17 Figs. Drawg. 17 Print Fig. 4
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner John C. [Signature] 3-20-05
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